

EAST - [Untitled1:1]

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L1: (173) 713/330.ccls.  
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DBs USPAT:US-PGPUB:EPO:JPO:DERWENT:IBM:TD8

Plurals

Default operator: OR

Highlight all hit terms initially

713/330.ccls.

BR5 f... IS&R... Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020133729 A1	20020919	9	Method for determining transition points on	713/330	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20020095611 A1	20020718	8	System and method for remotely power cycling a	713/330	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20020087906 A1	20020704	10	CPU power sequence for large multiprocessor systems	713/330	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20020066048 A1	20020530	12	Portable system arrangements having dual	713/330	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20020035703 A1	20020321	23	Electronic apparatus and processing ability	713/330	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20010008018 A1	20010712	31	Disk system and power-on sequence for the same	713/330	711/114
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6457131 B2	20020924	17	System and method for power optimization in parallel	713/300	713/330; 713/340
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6449724 B1	20020910	14	Portable information processing apparatus with a	713/300	713/323; 713/330;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6446213 B1	20020903	21	Software-based sleep control of operating system directed	713/300	713/1; 713/2;
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6425028 B1	20020723	8	Method and apparatus for activating a power interlock		713/324; 713/330;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6415389 B2	20020702	13	Jumperless computer system	713/330	710/300; 710/301;

Hits Details HTML

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